

2021 TSMC CyberShuttle Service Plan

Version: 2H V1.1

Shuttle Type (Technology)	Tape-In Date (TWN Time)/ TM # / Fab / Captain / Technology Availability											
	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
3 nm (*) Logic, Fin FET (0.75/1.2V)								Aug-9 TMNS98 FAB18 ZR V				
4 nm (*) Logic, Fin FET (0.75/1.2V)			Mar-16 TMNS95 Fab18 FK V					Aug-20 TMNS96 Fab18 Shirley V				
5 nm (*) Logic, FinFET (FF) (0.75/1.2V)		Feb-25 TMNT51 Fab18 Jeff V		Apr-15 TMNT52 Fab18 Shirley V			Jul-15 TMNT53 Fab18 Lynn V			Oct-5 TMNT54 Fab18 FK V		
6 nm (*) Logic, Fin FET (0.75/1.8V)				Apr-28 TMNT66 Fab15 Francis V				Aug-18 TMNT67 Fab15 Vincent V				Dec-22 TMNT68 Fab15 Jerry V
7 nm (*) Logic, Fin FET (0.75/1.8V)			Mar-3 TMNT91 Fab15 Vincent V			Jun-16 TMNT92 Fab15 Jerry V		Sep-22 TMNT93 Fab15 CC V				
12 nm Logic, FinFET Compact (0.8/1.8V) Logic, FinFET Compact Plus (0.8/1.8V)		Feb-22 TMNS15 Fab14 Francis V		Apr-13 TMNS16 Fab14 Lynn V		Jun-1 TMNS17 Fab14 CC V		Aug-3 TMNS18 Fab14 CC V		Oct-5 TMNS19 Fab14 Jeff V		Dec-7 TMNS20 Fab14 Francis V
16 nm Logic, FinFET Compact (0.8/1.8V) Logic, FinFET Compact Plus (0.8/1.8V) Logic, FinFET Plus (0.8/1.8V) RF, FinFET Compact (0.8/1.8V)		Feb-22 TMNS15 Fab14 Francis V		Apr-13 TMNS16 Fab14 Lynn V		Jun-1 TMNS17 Fab14 CC V		Aug-3 TMNS18 Fab14 CC V		Oct-5 TMNS19 Fab14 Jeff V		Dec-7 TMNS20 Fab14 Francis V
22 nm Logic, ULP (0.8/1.8V, 0.8/2.5V) Logic, ULL (0.8/1.8V, 0.8/2.5V) RF ULP (0.8/1.8V) RF ULL (0.8/1.8V, 0.8/2.5V) MRAM ULL (0.8/1.8V, 0.8/2.5V) MRAM RF ULL (0.8/1.8V, 0.8/2.5V) ReRAM RF ULL (0.8/2.5V)		Feb-23 TMNS21 Fab15 CC V		Apr-14 TMNS22 Fab15 Jeff V		Jun-9 TMNS23 Fab15 ZR V		Aug-11 TMNS24 Fab15 FK V		Oct-13 TMNS25 Fab15 Francis V		Dec-8 TMNS26 Fab15 CC V
28 nm Logic, HPM (0.9/1.8V, 0.9/2.5V) Logic, HPC (0.9/1.8V, 0.9/2.5V) Logic, HPC Plus (0.9/1.8V, 0.9/2.5V) Logic, HP (0.85/1.8V, 0.85/2.5V) Logic, LP (1.05/1.8V, 1.05/2.5V) Logic, HPL (1.0&1.8V, 1.0/2.5V)	Jan-13 TMNT01 Fab15 ZR V	Feb-19 TMNT02 Fab15 Shirley V	Mar-17 TMNT03 Fab15 Jerry V	Apr-7 TMNT04 Fab15 CC V	May-12 TMNT05 Fab15 FK V	Jun-16 TMNT06 Fab15 Vincent V	Jul-14 TMNT07 Fab15 Jeff V	Aug-11 TMNT08 Fab15 Francis V	Sep-8 TMNT09 Fab15 FK V	Oct-20 TMNT10 Fab15 Jerry V	Nov-17 TMNT11 Fab15 Vincent V	Dec-15 TMNT12 Fab15 Lynn V

High Voltage (1.8/3.3/32V)	V					V				
High Voltage (1.8/5/32V)	V					V				
High Voltage, BCD (Generation-2)	V		V		V			V		V
High Voltage, BCD (Generation-3)	V		V		V			V		V
0.25 um	Jan-6		Mar-17		May-19		Jul-7		Sep-8	Nov-17
	TMNT60		TMNT61		TMNT62		TMNT63		TMNT64	TMNT65
	Fab10		Fab8		Fab10		Fab10		Fab8	Fab10
	YW		YW		CC		YW		YW	ZR
Logic, G (2.5/3.3V, 2.5/5V)	V		V		V		V		V	V
Mixed-Signal/RF, G (2.5/3.3V, 2.5/5V)	V		V		V		V		V	V
High Voltage, BCD (2.5/5/12/24/40V/Vg2.5/5V)	V		V		V		V		V	V
High Voltage, BCD (2.5/5/12/24/40/Vg2.5/5/12V)	V		V		V		V		V	V
High Voltage, BCD (2.5/5/60V/Vg 2.5/5V)	V		V		V		V		V	V
High Voltage, BCD (2.5/5/12/24/40/60/Vg2.5/5/12V)	V		V		V		V		V	V
High Voltage, Gen-2 BCD (2.5/5/7/12/20/24/40/45/60V/Vg 2.5/5/12V)	V		V		V		V		V	V
0.35 um: Part 1	Jan-20				May-5				Aug-25	Nov-24
	TMNT31				TMNT32				TMNT33	TMNT34
	Fab3				Fab10				Fab3	Fab10
	Vincent				JR				CC	YW
Logic, G, Polycide/Silicide (3.3/5V)	V				V				V	V
Mixed-Signal, G (3.3/5V)	V				V				V	V
High Voltage, G, DDD (3.3/12/13.5)	V				V				V	V
High Voltage, DDD (3.3/12/13.5/15/18V)	V				V				V	V
High Voltage, BCD (3.3/20/23/Vg3.3V)	V				V				V	V
High Voltage, BCD (3.3/5/12/15/20/40/Vg3.3/5/12V)	V				V				V	V
0.35 um: Part 2			Mar-3		May-19				Sep-14	Nov-3
			TMNT35		TMNT36				TMNT37	TMNT38
			Fab3		Fab3				Fab3	Fab3
			Jerry		Lynn				Jerry	YW
SiGe BiCMOS, G (3.3V)			V		V				V	V
0.5 um: Part 1				Apr-7		Jun-16			Sep-29	Dec-8
				TMNT55		TMNT56			TMNT57	TMNT58
				Fab3		Fab3			Fab3	Fab3
				YW		YW			YW	YW
High Voltage, (5/20/450/600/700/800V)				V		V			V	V

* The calendar is Taiwan time

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Special Note (*)

1. tsmc owns the right to adjust shuttle plan and will inform customer about the change in 3 months ahead.